Abstract

A method of reducing a phase error caused by a plurality of error sources in a signal which is present in a digital frequency representation in the form of a sequence of a plurality of digital partial signals which are associated with a number of subcarriers (k) of a carrier. The following steps are performed for each partial signal: equalization of the partial signal (Y(i,k)), estimation of the phase error of the equalized partial signal (X(i,k)), and correction of the estimated phase error of the equalized partial signal. An embodiment of that method provides that the equalization step includes the elimination of an accumulation of a phase error of the partial signal, caused by a sampling frequency error, over the sequence of the partial signals, such that the accumulation is negligible. In addition the estimation step includes a step of detecting a plurality of predetermined pilot signals and a step of determining a phase correction factor on the basis of the detected pilot signals, wherein at least one multiplication operation is carried out solely by means of shift and adding operations. A corresponding apparatus is also described.